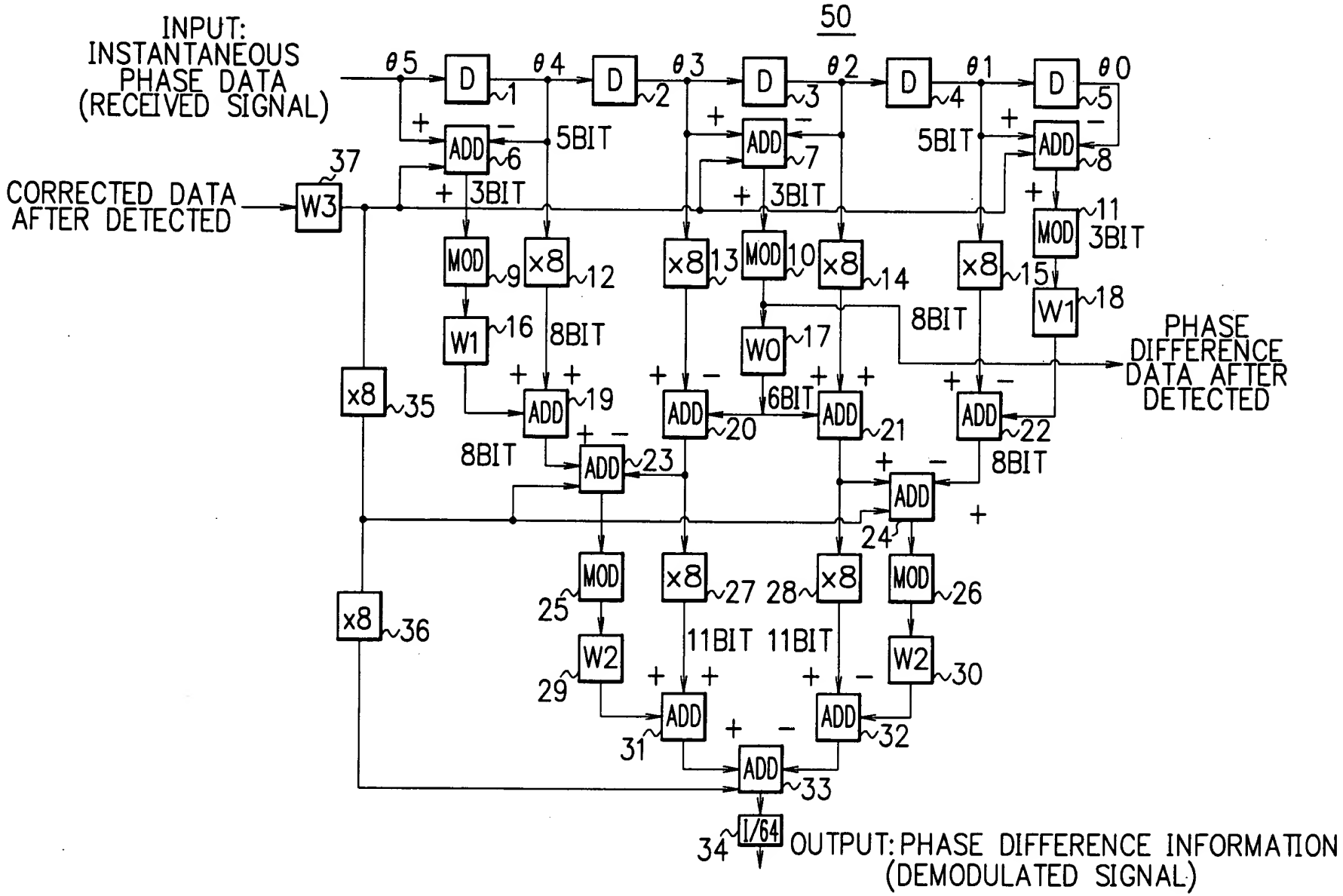
FIG. 1


50
INPUT:
INSTANTANEOUS
PHASE DATA
(RECEIVED SIGNAL)
CORRECTED DATA
AFTER DETECTED
θ5
θ4
θ3
θ2
θ1
θ0
D
ADD
MOD
W1
W2
W3
W0
x8
1/64
5BIT
3BIT
8BIT
6BIT
11BIT
PHASE
DIFFERENCE
DATA AFTER
DETECTED
OUTPUT: PHASE DIFFERENCE INFORMATION
(DEMODULATED SIGNAL)